

<b>Form PTO-1449</b>  <b>U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)	<b>ATTY. DOCKET NO.</b>  01 P 14516 US (8055-26)	<b>SERIAL NO.</b> 09/931125 Not Yet Assigned
	<b>APPLICANT</b>  Randall Rooney et al.	
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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)			
			Schanstra, Ivo, et al., <u>Semiconductor Manufacturing Process Monitoring Using Built-In Self-Test for Embedded Memories</u> —
			Vollrath, Jorg, et al., <u>Compressed Bit Fail Maps for Memory Fail Pattern Classification</u> —
			Vollrath, Joerg, <u>Tutorial: Characterizing SDRAMs</u> —

<b>EXAMINER</b> 	<b>DATE CONSIDERED</b> 1/23/04
* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	